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2ZBI DMA ON  
 2ZC1 ADR4  
 2ZC1 ADR5  
 2ZC1 ADR6  
 2ZC1 ADR7

2ZC1 DMA ON  
 2ZAI IOR/  
 2ZAI IOW/

2ZB1 DATA0  
 DATA1  
 DATA2  
 DATA3  
 DATA4  
 DATA5  
 DATA6  
 DATA7

2ZD1 ADR0  
 2ZD1 ADR1

2ZD5 RESET  
 2ZC1 DMA ACK I/

2ZD1 22.032MHz

2ZA3 RESET /

FLOPPY CONTROLLER

I/O PORT DECODE

DATA SEPARATION

SELECT MISCIN/ 5ZCB  
 SELECT KD/ 5ZBB  
 SELECT STATUS/ 4ZBB  
 SELECT DBBIN/ 4ZAB  
 SELECT 8275/ 10ZBB  
 SELECT 8253/ 1ZDB  
 SELECT 8257/ 2ZC1

SELECT DBBOUT/ 4ZBB  
 CLOCK F0/ 4ZCB  
 SET F1/ 4ZCB  
 RESET F1/ 4ZCB  
 START TIMER/ 1ZCB  
 SELECT MISCOUT/ 5ZDB  
 DMA REQ1 2ZC1

TEST POINT  
 SELECT I/  
 SELECT O/  
 DIRECTION/  
 STEP/  
 WRITE DATA/  
 WRITE ENABLE/

2MHz 1ZCB  
 LOW CURRENT

11.016 MHz 10ZBB  
 11.016 MHz/ 10ZCB