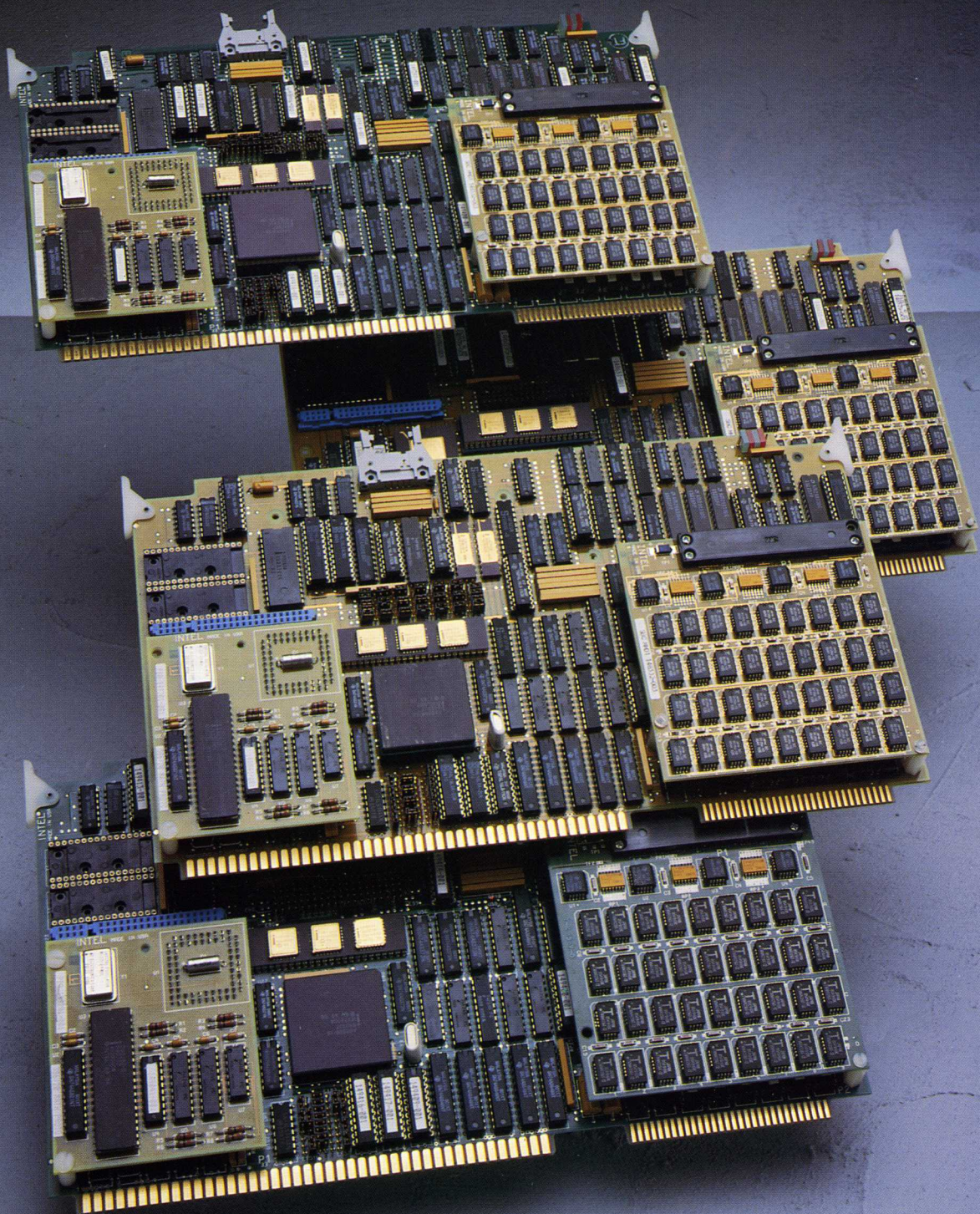
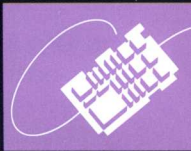




The iSBC[®] 386 Series

*Intel's highest performance
MULTIBUS[®] CPU boards*





iSBC[®] 386/21/22/24/28 SERIES SINGLE BOARD COMPUTERS

- **16 MHz 80386 Microprocessor**
2.2x performance of 8 MHz 80286
Object code compatibility with
8086/80286 software eases upgrades
- **Available with 1, 2, 4, or 8M Bytes of On-board, 32-Bit Memory**
iSBC MM-series memory modules save space/power and lower system cost
- **80287 Floating Point Math Coprocessor**
Speeds all floating point math operations
- **64K Byte Cache Memory**
Provides 0 wait-state reads for the 80386
- **Two 32-pin JEDEC Sites for up to 512K Bytes of EPROM Memory**
Ample capacity for non-volatile storage of application code, debug/test software, and OS kernels
- **RS232C Interface**
Supports local or remote terminal (via a modem)
- **iSBX™ Interface**
Allows for low-cost I/O expansion

With performance more than double that of an 8 MHz, 80286-based system, the iSBC 386/2x series is Intel's highest-performance line of MULTIBUS[®] CPU boards. These boards feature a 16 MHz 80386 CPU, an 80287 math coprocessor, a 64K Byte 0 wait-state cache memory to support the CPU, and a 32-bit interface to 1, 2, 4, or 8M Bytes of dual-ported parity DRAM memory. An additional 1, 2, 4, or 8 M Byte iSBC MM-series memory module may be installed to provide up to 16M Bytes of on-board DRAM memory. The iSBC

386/2x boards also feature an 8/16-bit iSBX MULTIMODULE™ interface for low-cost I/O expansion, an asynchronous RS232C interface to support a local terminal or modem, two 16-bit programmable timer/counters, a 16-level direct-vectored interrupt controller, two 32-pin JEDEC sites for up to 512K Bytes of EPROM memory, and multimaster MULTIBUS arbitration logic. The iSBC 386/21/22/24/28 boards are ideal for applications needing 32-bit performance together with full MULTIBUS I compatibility.

PRODUCT DETAILS

- iSBC 386/21** 16 MHz 80386-based MULTIBUS I CPU board with 1M Byte of on-board DRAM memory
- iSBC 386/22** 16 MHz 80386-based MULTIBUS I CPU board with 2M Bytes of on-board DRAM memory
- iSBC 386/24** 16 MHz 80386-based MULTIBUS I CPU board with 4M Bytes of on-board DRAM memory
- iSBC 386/28** 16 MHz 80386-based MULTIBUS I CPU board with 8M Bytes of on-board DRAM memory

UNITED STATES
Intel Corporation
3065 Bowers Avenue,
Santa Clara, CA 95051

JAPAN
Intel Japan K.K.
5-6 Tokodai Toyosato-machi
Tsukuba-gun, Ibaraki-ken 300-26
Japan

FRANCE
Intel Paris
1 Rue Edison, BP 303
78054 Saint-Quentin en Yvelines
France

UNITED KINGDOM
Intel Corporation (U.K.) Ltd.
Piper's Way
Swindon
Wiltshire, England SN3 1RJ

WEST GERMANY
Intel Semiconductor GmbH
Seidlstrasse 27
D-8000 Munchen 2
West Germany

Intel Corporation assumes no responsibility for the use of any circuitry other than circuitry embodied in an Intel product. No other circuit patent licenses are implied. Information contained herein supersedes previously published specifications on these devices from Intel.