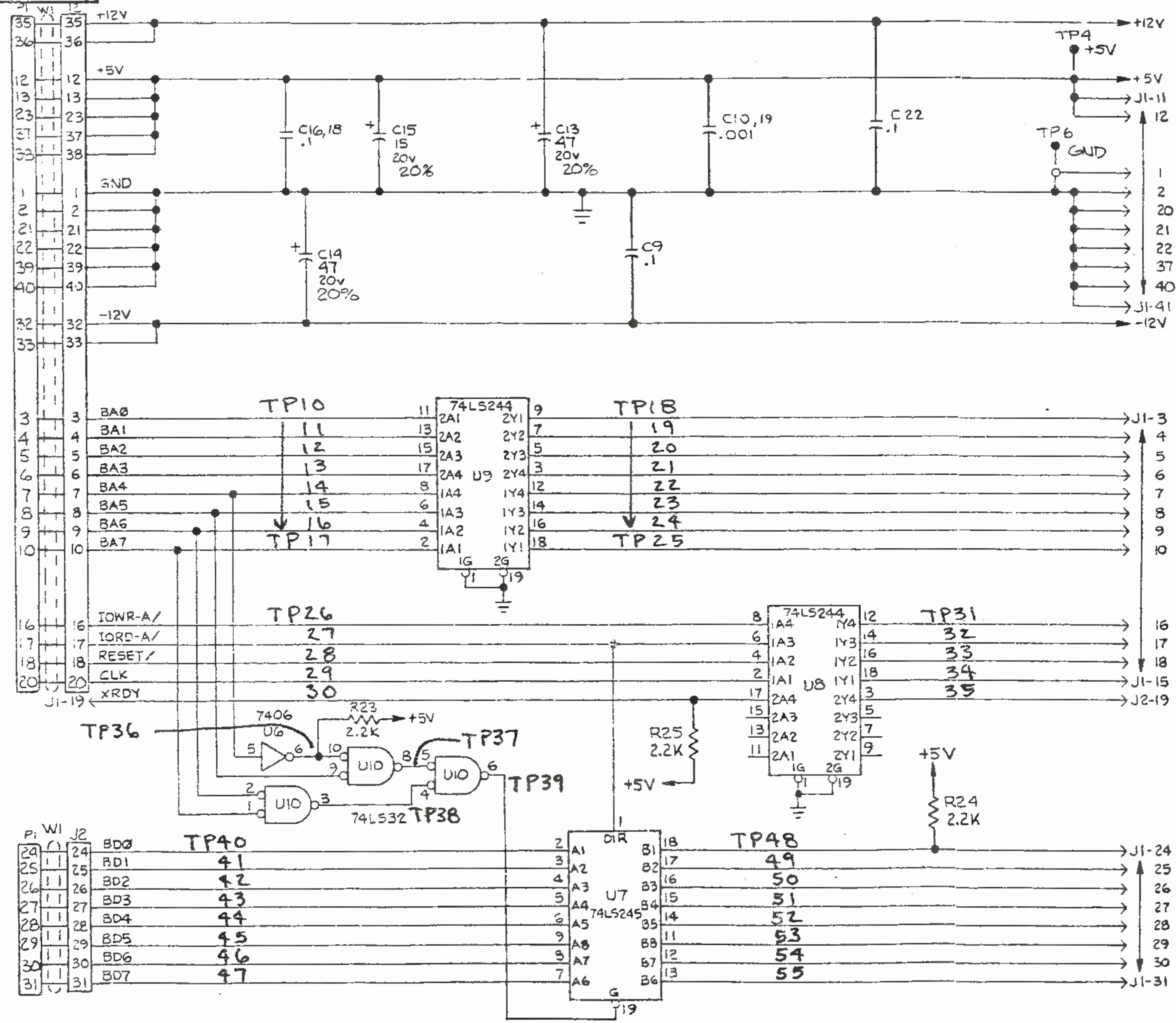


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REVISIONS							
REV	DESCRIPTION	DFT	DATE	CHK	DATE	APVD	DATE
A	ECO 14-1278	J	8/14/82	PJA	7/19/82	JD	8/11/82
B	ECO 14-1217	PBD	7/23/82	JJA	8/4/81	JD	1/21/82



DEVICE TABLE						
REFERENCE DESIGNATOR	DEVICE TYPE	GROUND	POWER	PINS		UNUSED LOGIC ELEMENT OUTPUT PINS
			+5V	+12V	-12	
U6	7406	7	14	-	-	U6-7
U10	74LS32	7	14	-	-	U10-1
U8,9	74LS244	10	20	-	-	U8-5,7,9
U7	74LS245	10	20	-	-	
U1	723	7	-	11,12	-	
U4	723	7	-	-	-	
U3	LM358	4	-	8	-	
U2	MC1403	3	-	1	-	
U5	75188	7	-	14	1	U5-8,11

- NOTES: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTOR VALUES ARE IN OHMS, 1/4W, 5%
 2. ALL CAPACITOR VALUES ARE IN MICROFARADS, 780% - 20%, 50V.
 3. RPI-4 IS A 1K, SPARE RESISTOR.
 4. ON 163788-001 R16 IS 4.3 OHMS, 1/4W, 5%
ON 163788-002 R16 IS 3.65 OHMS, 1/4W, 1%

163788-002
163788-001
ASSEMBLIES USING THIS SCHEMATIC

REF DESIGNATOR	NOT USED
RPI	
C22	C17
CR5	
R27	
Q8	Q2
U10	
LAST USED	NOT USED

QUANTITY PER DASH NO.		ITEM NO.		DESCRIPTION	
UNLESS OTHERWISE SPECIFIED:		SIGNATURE		DATE	
1. DIMENSIONS ARE IN INCHES.		DRNBY: [Signature]		8/14/82	
2. BREAK ALL SHARP EDGES.		CHK BY: [Signature]		9/8/82	
3. DO NOT SCALE DRAWING.		ENGR: [Signature]		7/12/82	
4. TOLERANCES:		APVD			
ANGLES ±		APVD			
XX ±					
XXX ±					
REF DESIGNATOR		TITLE		SANTA CLARA CALIF. 95051	
		intel®		SHEET 1 OF 2	
		SHEET 1 OF 2		SCALE	
		SIZE DCLC		CL CODE	
		D 14		01000	
		PLANE LVL		DOCUMENT NUMBER	
				163739	
		REV		8	

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