

121746-9

Figure 3-3. SerDes Board Block Diagram

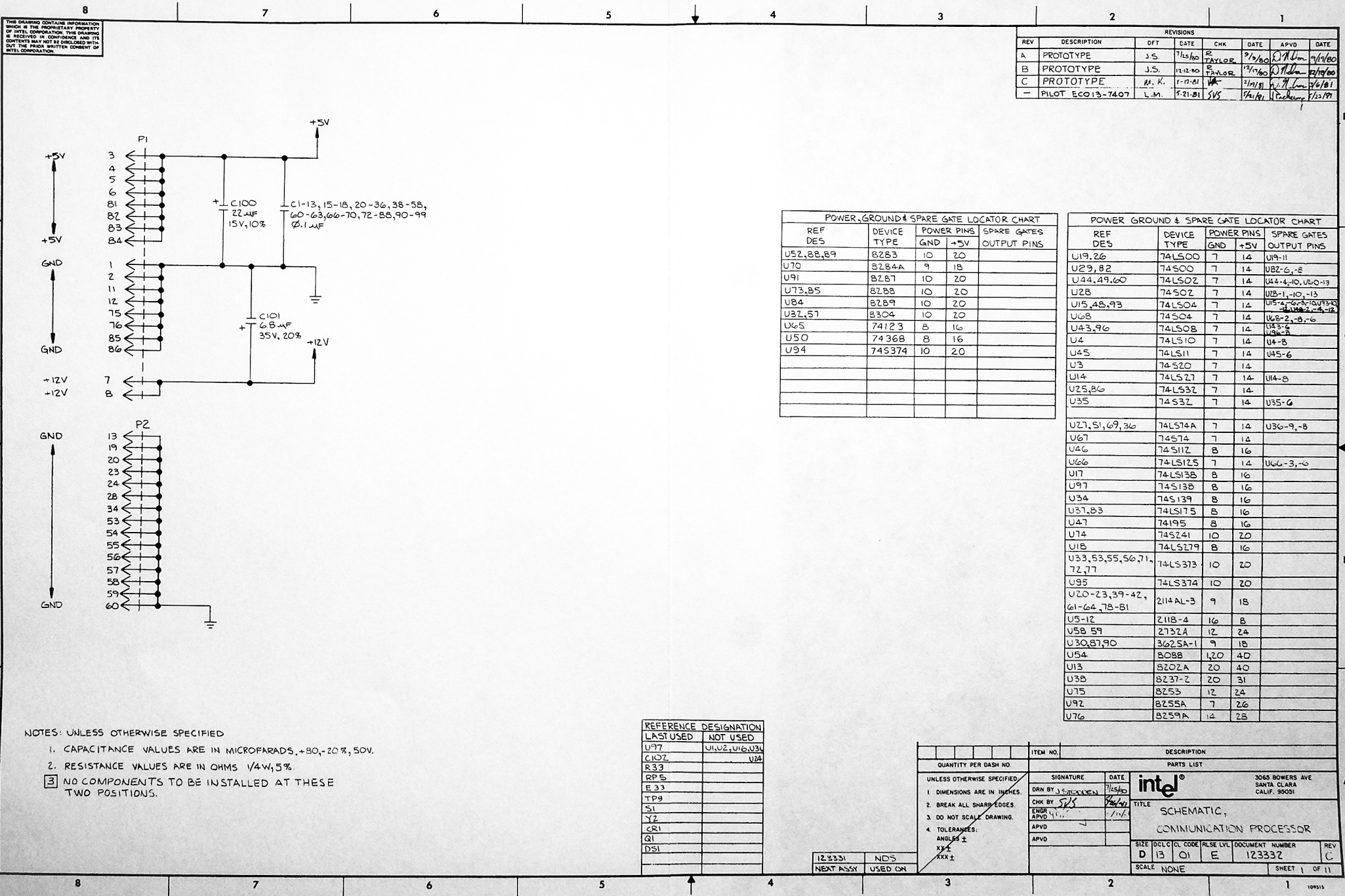


Figure 6-1. Processor Board Schematic Diagram (Sheet 1 of 11)

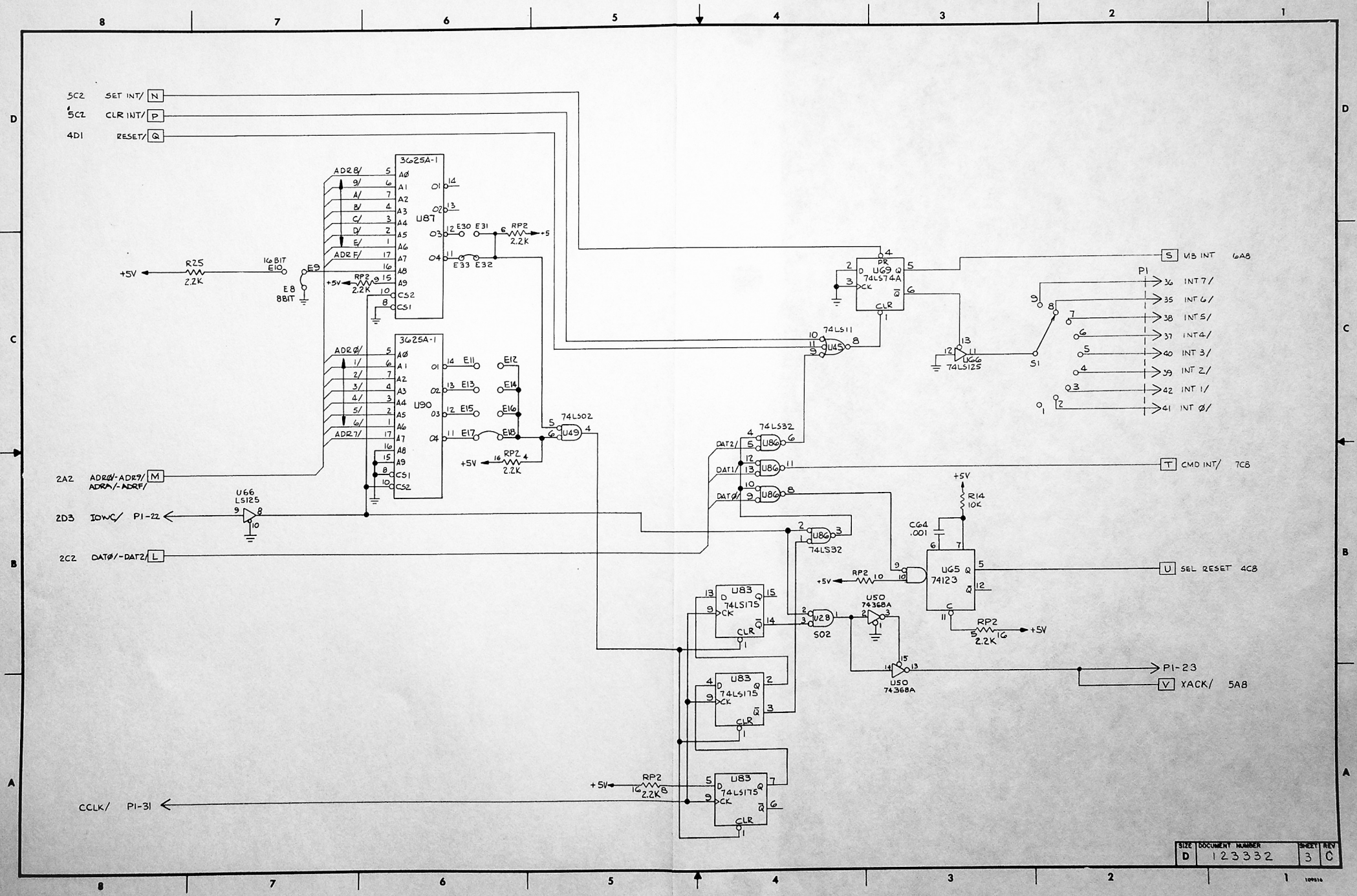
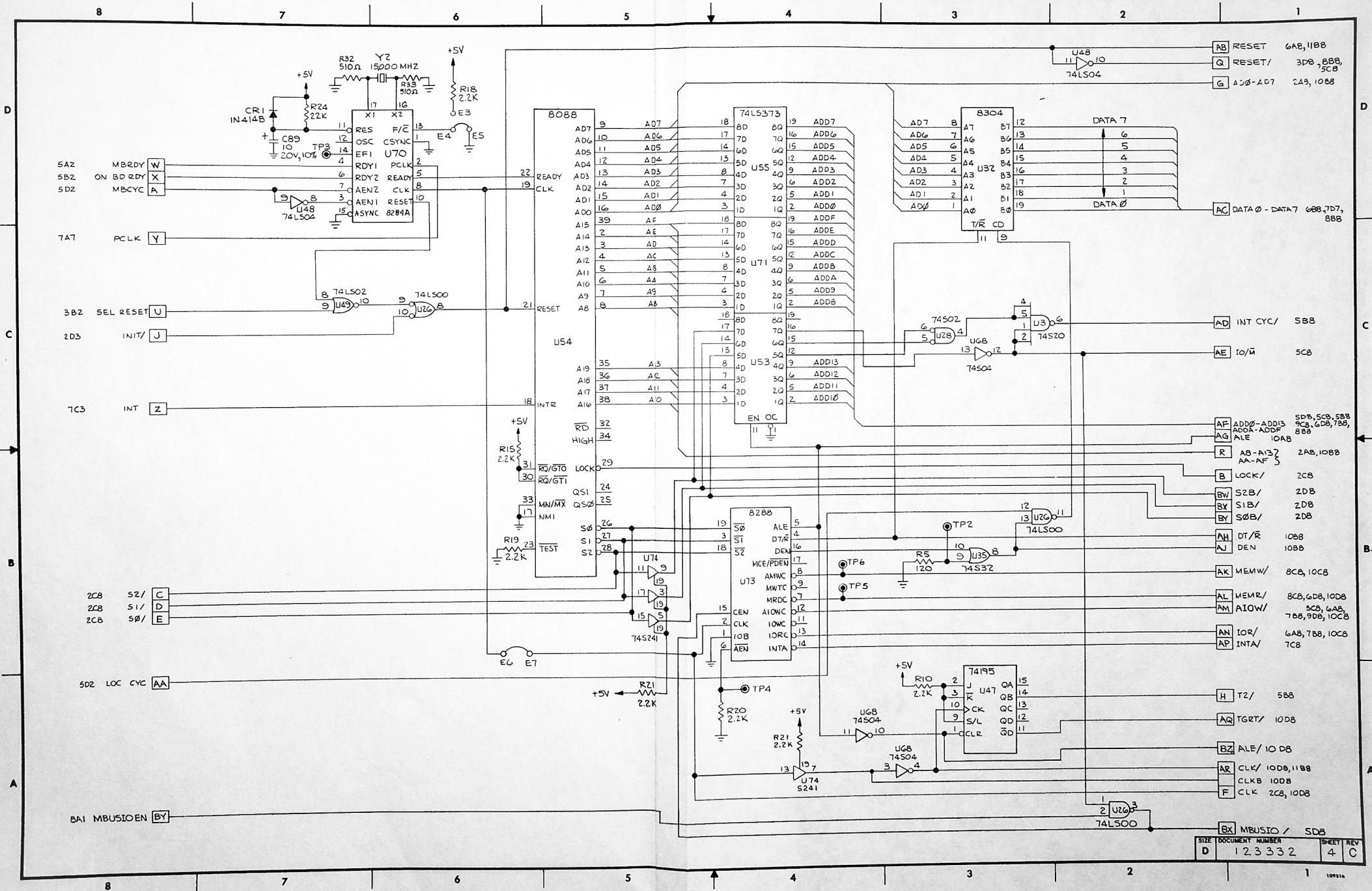


Figure 6-1. Processor Board Schematic Diagram (Sheet 3 of 11)



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| SIZE | DOCUMENT NUMBER | SHEET | REV |
| D | 123332 | 4 | C |

Figure 6-1. Processor Board Schematic Diagram (Sheet 4 of 11)

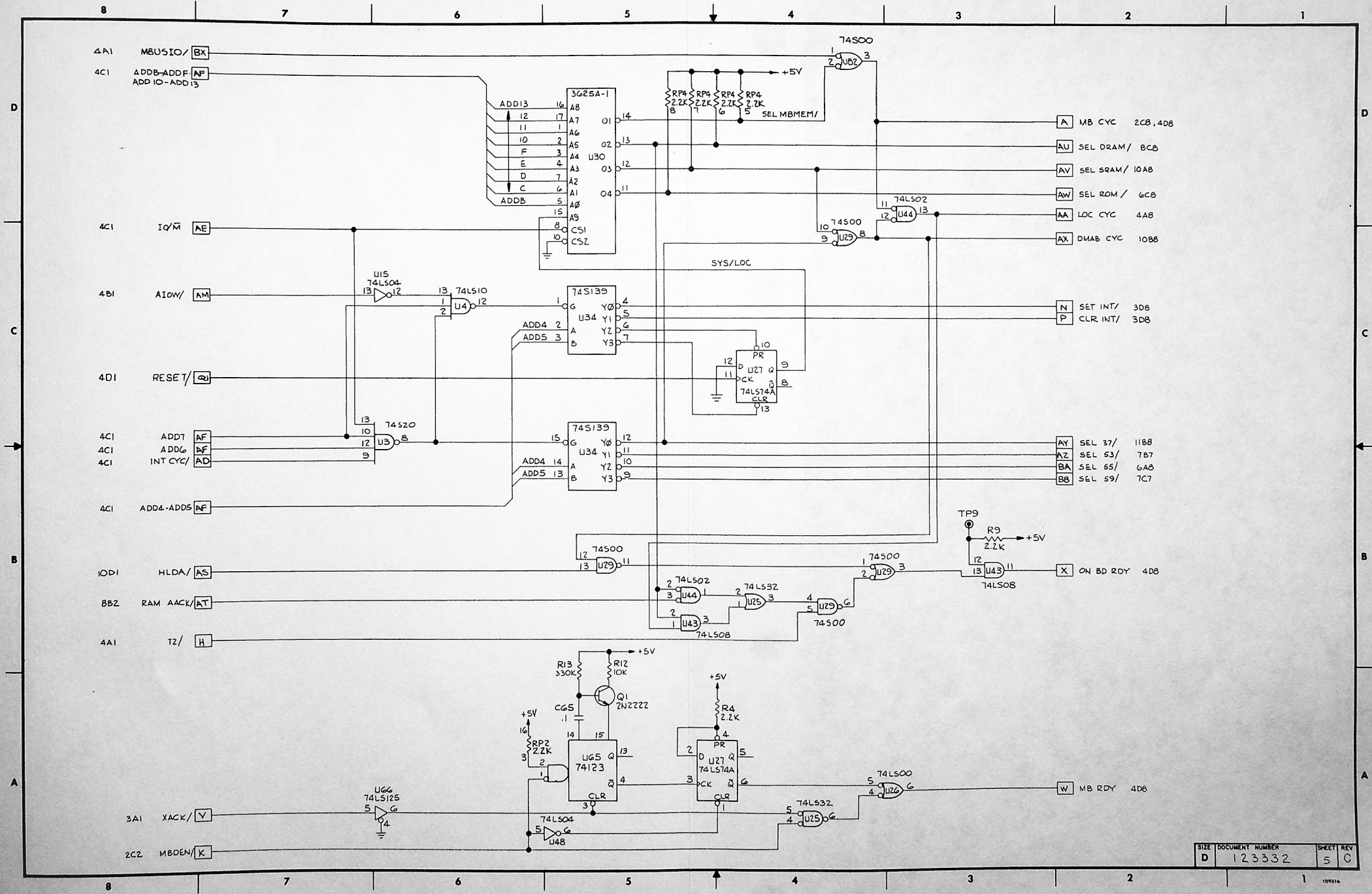
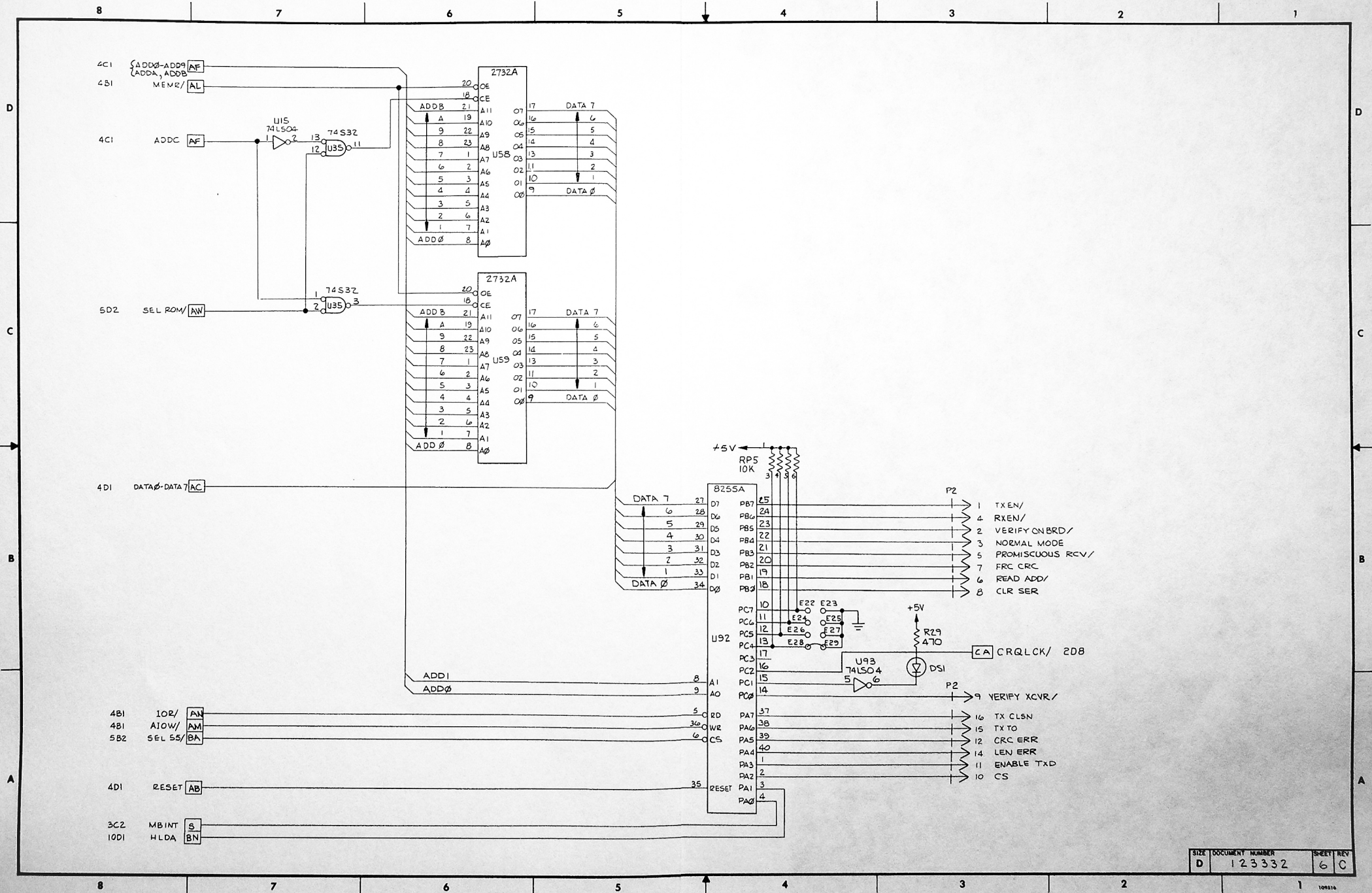


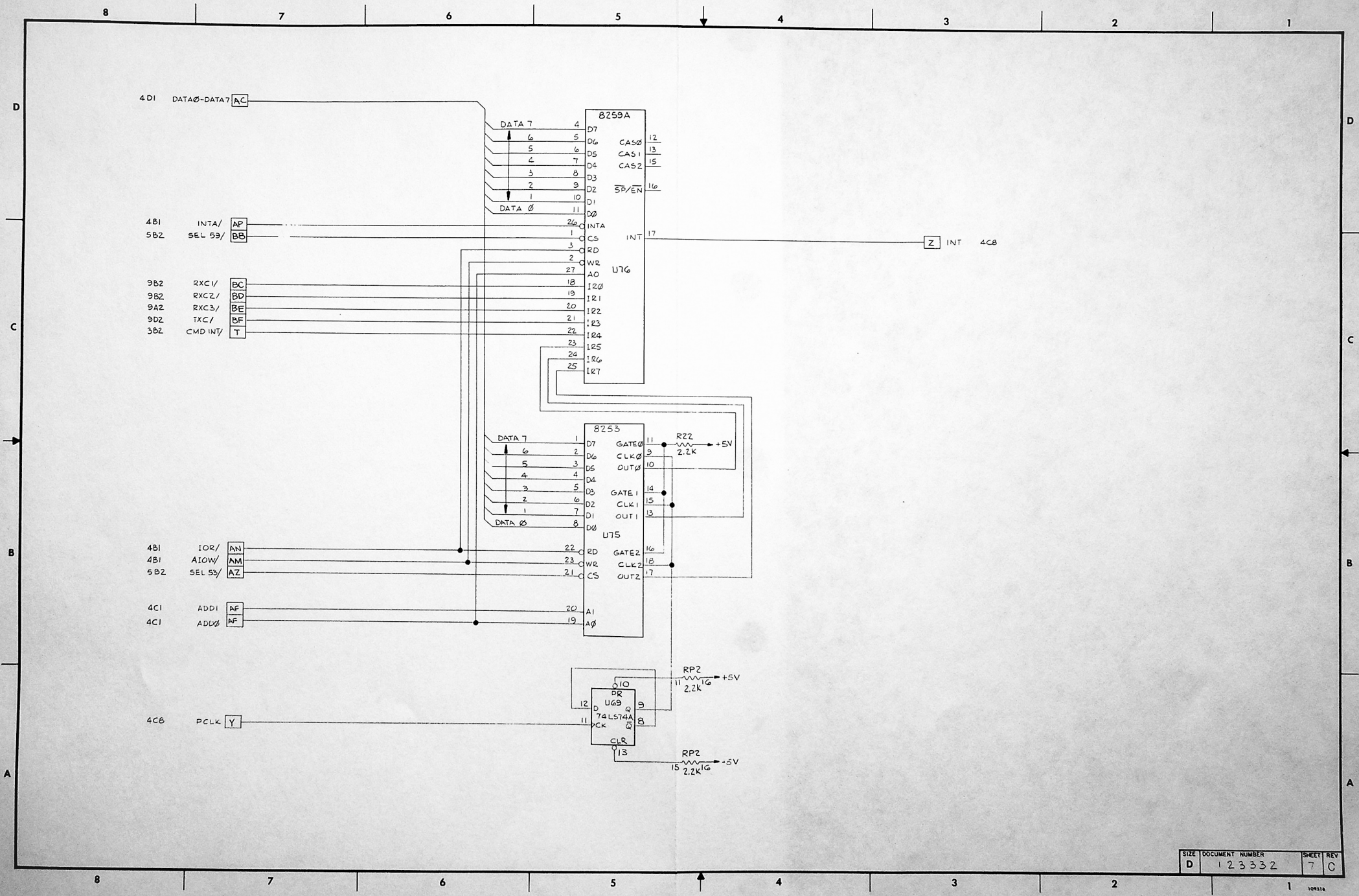
Figure 6-1. Processor Board Schematic Diagram (Sheet 5 of 11)

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| D | 123332 | 5 C |



| SIZE | DOCUMENT NUMBER | SHEET | REV |
|------|-----------------|-------|-----|
| D | 123332 | 6 | C |

Figure 6-1. Processor Board Schematic Diagram (Sheet 6 of 11)



| SIZE | DOCUMENT NUMBER | SHEET REV |
|------|-----------------|-----------|
| D | 123332 | 7 C |

Figure 6-1. Processor Board Schematic Diagram (Sheet 7 of 11)

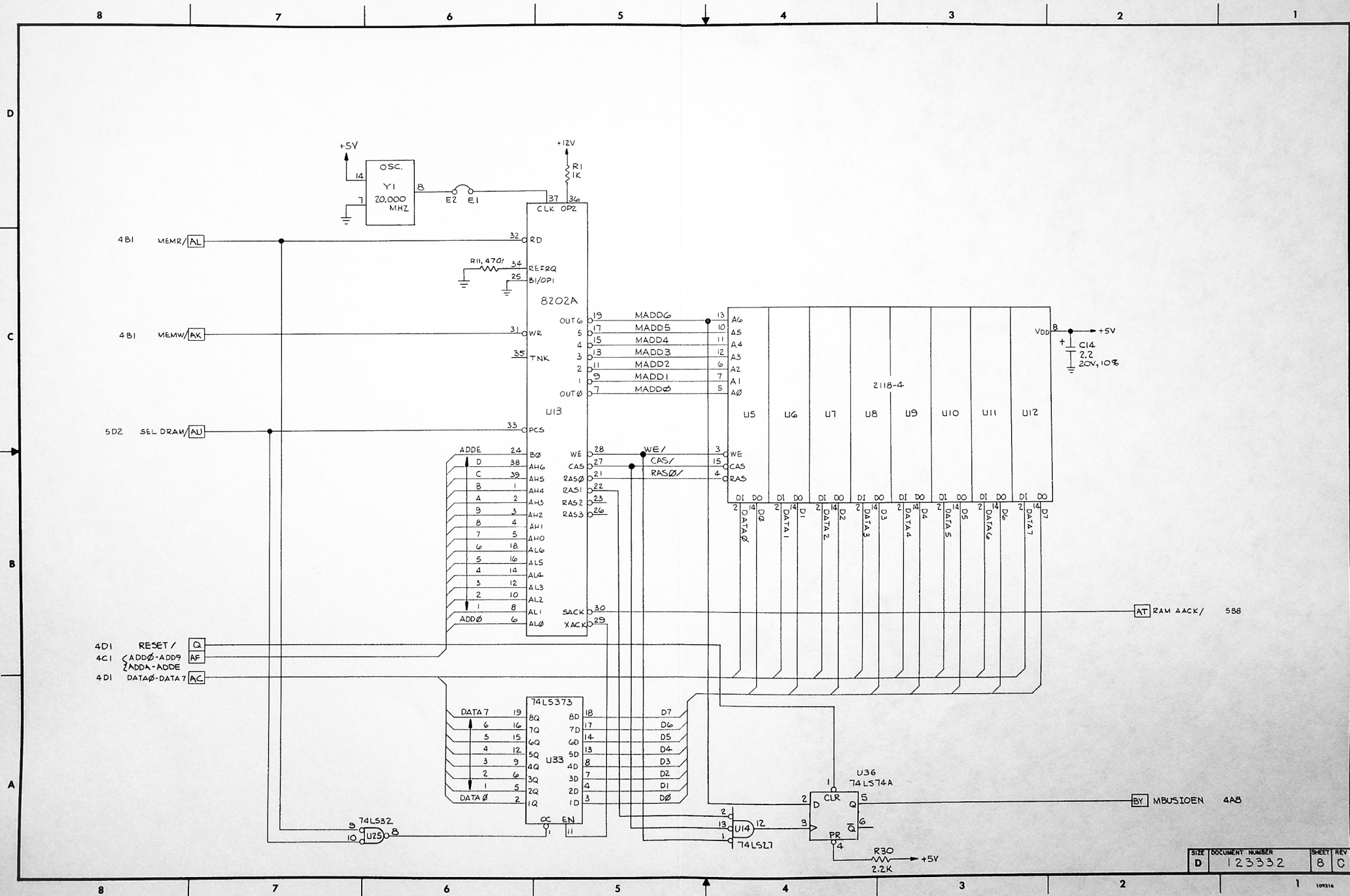
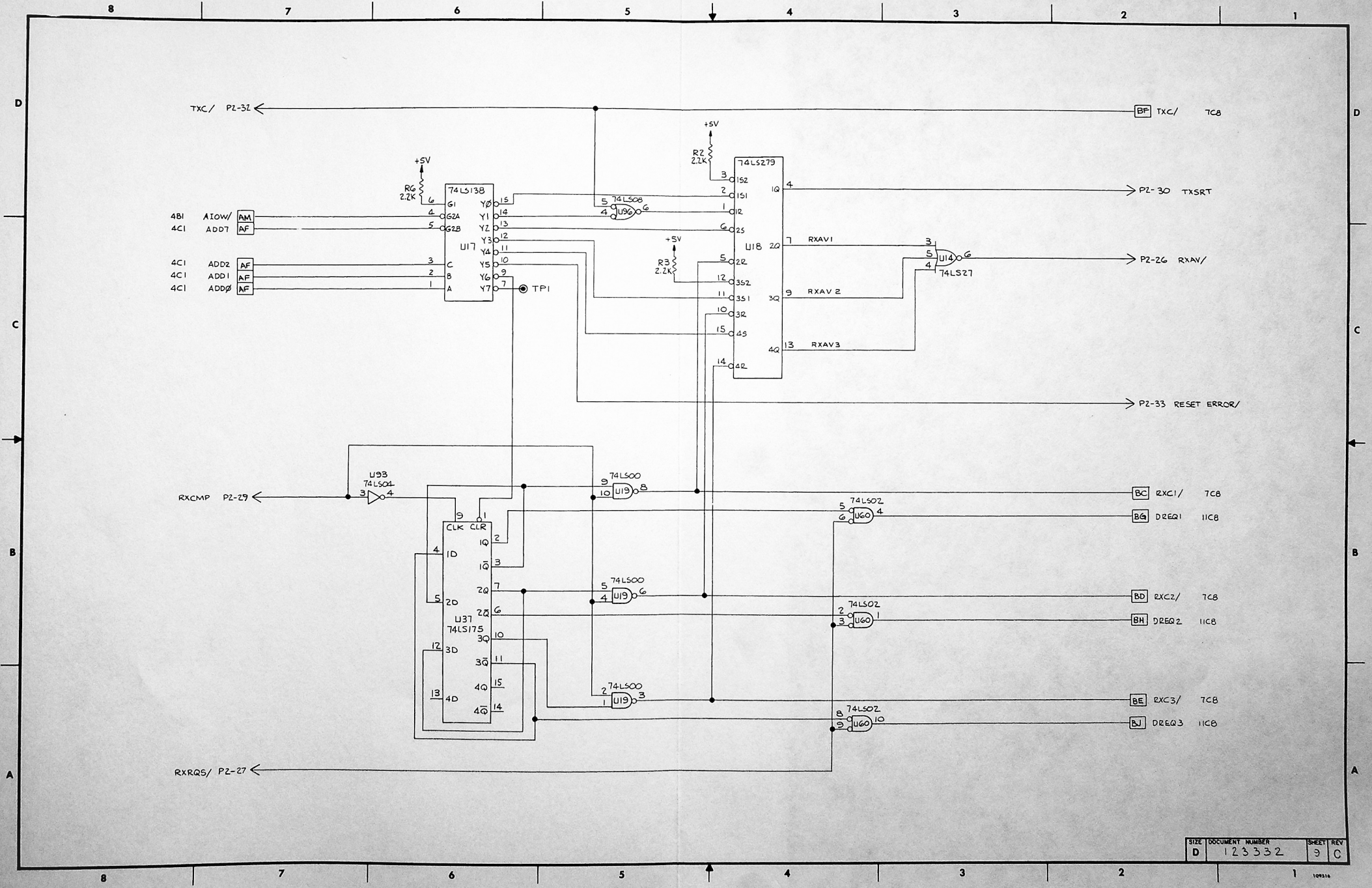


Figure 6-1. Processor Board Schematic Diagram (Sheet 8 of 11)



| SIZE | DOCUMENT NUMBER | SHEET | REV |
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| D | 123332 | 9 | C |

Figure 6-1. Processor Board Schematic Diagram (Sheet 9 of 11)

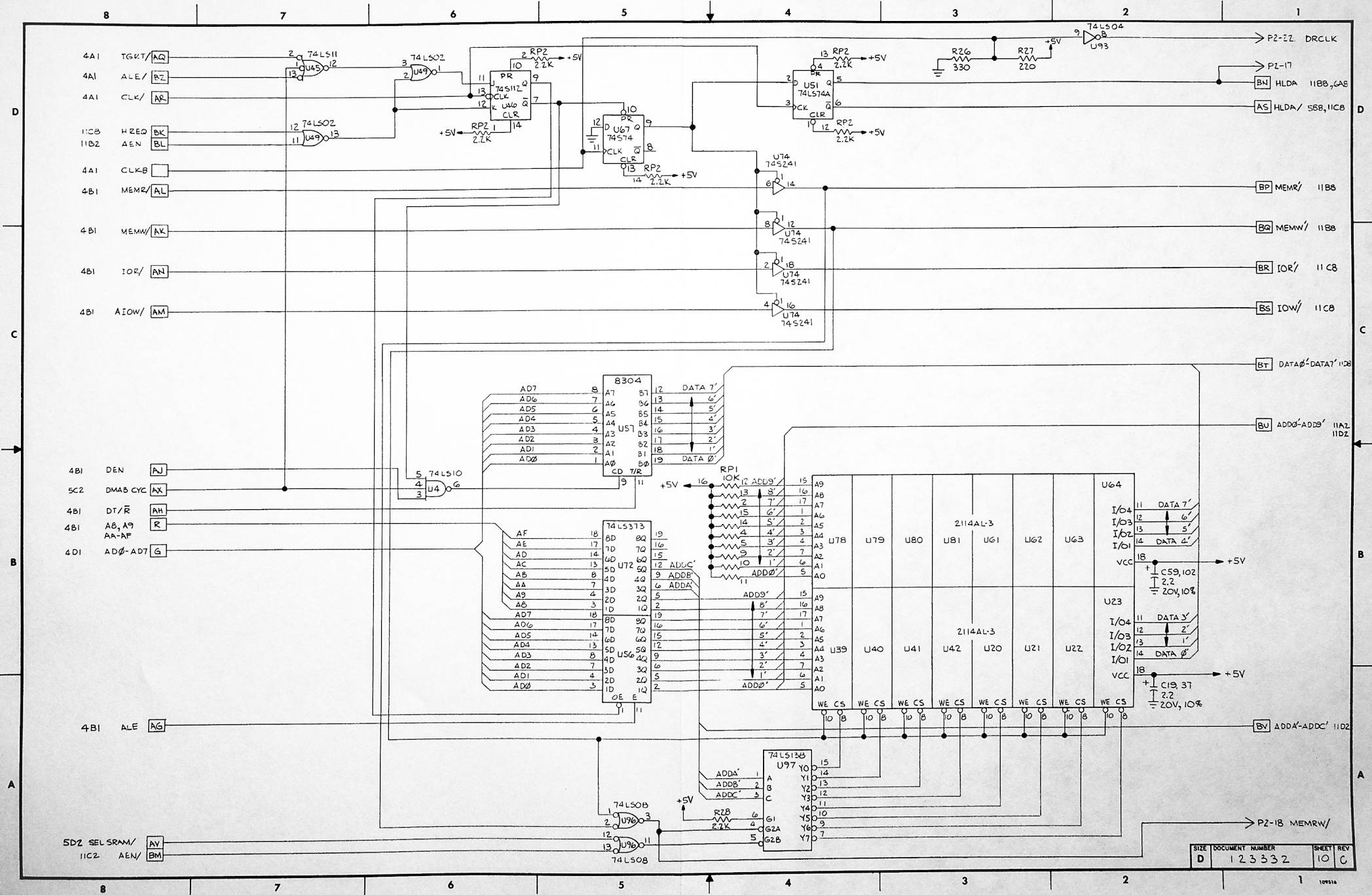
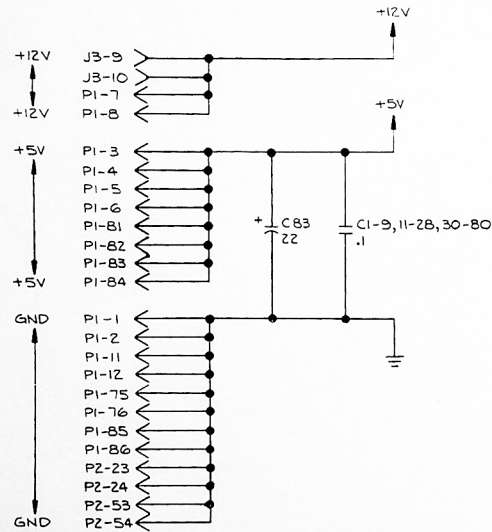


Figure 6-1. Processor Board Schematic Diagram (Sheet 10 of 11)

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| SIZE | DOCUMENT NUMBER | SHEET REV |
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| REVISIONS | | | | | | | |
|-----------|------------------|---------|--------|-----|---------|--------|---------|
| REV | DESCRIPTION | QFT | DATE | CHK | DATE | APVD | DATE |
| C | PILOT ECO13-73B3 | J ROCHA | 3-4-81 | EG | 3-31-81 | J.P.D. | 5-21-81 |



| REFERENCE DESIGNATION | |
|-----------------------|--------------|
| LAST USED | NOT USED |
| C84 | C8 |
| DL3 | |
| J3 | |
| R82 | |
| RP3 | |
| U93 | U18,23,41,42 |
| Y1 | |
| P2 | |
| E19 | |

| DEVICE TABLE | | | | |
|----------------------------|-------------|------------|------|---|
| REF DES | DEVICE TYPE | POWER PINS | | UNUSED LOGIC ELEMENT OUTPUT PINS |
| | | GND | +5V | |
| U14 | 10102 | 8 | 1,16 | U14-15 |
| U5 | 10105 | 8 | 1,16 | |
| U24 | 10107 | 8 | 1,16 | U24-12,13 |
| U6,13,50 | 10116 | 8 | 1,16 | U13-2,3,6,7 U50-14,15 |
| U25 | 10131 | 8 | 1,16 | U25-14,15 |
| U32 | 10135 | 8 | 1,16 | |
| U31,60 | 261532 | 8 | 16 | U31-5,11,13 U60-13 |
| U75 | 74500 | 7 | 14 | U75-8 |
| U43,55,74,76,86 | 74LS00 | 7 | 14 | U43-11 U74-6 U76-3,6,11 |
| U19 | 74502 | 7 | 14 | U19-1 |
| U0,30,54 | 74LS02 | 7 | 14 | U54-13 |
| U63 | 74504 | 7 | 14 | U63-12 |
| U3,49,58,87 | 74LS04 | 7 | 14 | U3-12 U58-10 U49-2 |
| U71 | 74508 | 7 | 14 | |
| U48 | 74LS10 | 7 | 14 | U48-6,8 |
| U22 | 74LS11 | 7 | 14 | U22-8,12 |
| U26 | 74530 | 7 | 14 | |
| U15,38 | 74532 | 7 | 14 | U15-3,8,11 |
| U57,72 | 74LS32 | 7 | 14 | U57-3,6,8 U72-8 |
| U66 | 7438 | 7 | 14 | U66-11 |
| U85 | 7451 | 7 | 14 | |
| U4,17,33,70,83,84,90,91,92 | 74574 | 7 | 14 | U33-8,9 U17-8,9 U70-8,9 U90-5,6 U91-5,6 U92-8,9 |
| U21,35,53,56,73,81,93 | 74LS74 | 7 | 14 | U53-8,9 U93-8,9 |
| U20,29,44,47,64 | 74586 | 7 | 14 | U20-3,6,8 U64-6 |
| U40,67 | 745112 | 8 | 16 | U40-5,6 U67-7,9 |
| U12 | 74LS112 | 8 | 16 | U12-5,6 |
| U28 | 74LS133 | 8 | 16 | |
| U68 | 74510 | 7 | 14 | |
| U77 | 74LS40 | 7 | 14 | U77-6 |
| U69 | 745153 | 8 | 16 | |
| U59,62 | 745157 | 8 | 16 | |

| DEVICE TABLE | | | | |
|---------------|-------------|------------|-----|----------------------------------|
| REF DES | DEVICE TYPE | POWER PINS | | UNUSED LOGIC ELEMENT OUTPUT PINS |
| | | GND | +5V | |
| U39 | 74LS161 | 8 | 16 | |
| U78 | 74LS164 | 7 | 14 | |
| U16 | 74LS164 | 7 | 14 | |
| U2,89 | 74166 | 8 | 16 | |
| U52 | 74LS174 | 8 | 16 | |
| U36,37,79,80 | 74LS175 | 8 | 16 | |
| U7,27 | 745260 | 7 | 14 | |
| U8,34,5,46,51 | 74LS273 | 10 | 20 | U45-19 |
| U1 | 745288 | 8 | 16 | |
| U1,88 | 74123 | 8 | 16 | U88-12 |
| U34,61,81 | 74LS08 | 7 | 14 | U34-8 U61-8,11 U81-6 |
| U65 | 74LS21 | 7 | 14 | U65-8 |

NOTES: UNLESS OTHERWISE SPECIFIED.
 1. CAPACITANCE VALUES ARE IN MICROFARADS.
 2. RESISTANCE VALUES ARE IN OHMS, 1/4 W, 5%.

| | | |
|------------------------------|----------------------|-------------|
| QUANTITY PER DASH NO. | ITEM NO. | DESCRIPTION |
| UNLESS OTHERWISE SPECIFIED: | SIGNATURE | DATE |
| 1. DIMENSIONS ARE IN INCHES. | DRN BY J. ROCHA | 3-15-81 |
| 2. BREAK ALL SHARP EDGES. | CHK BY JPS | 3-15-81 |
| 3. DO NOT SCALE DRAWING. | ENGR APVD J. Pacheco | 5/1/81 |
| 4. TOLERANCES: | APVD | |
| ANGLES ± | | |
| XX ± | | |
| XXX ± | | |

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 3065 BOWERS AVE
 SANTA CLARA
 CALIF. 95051

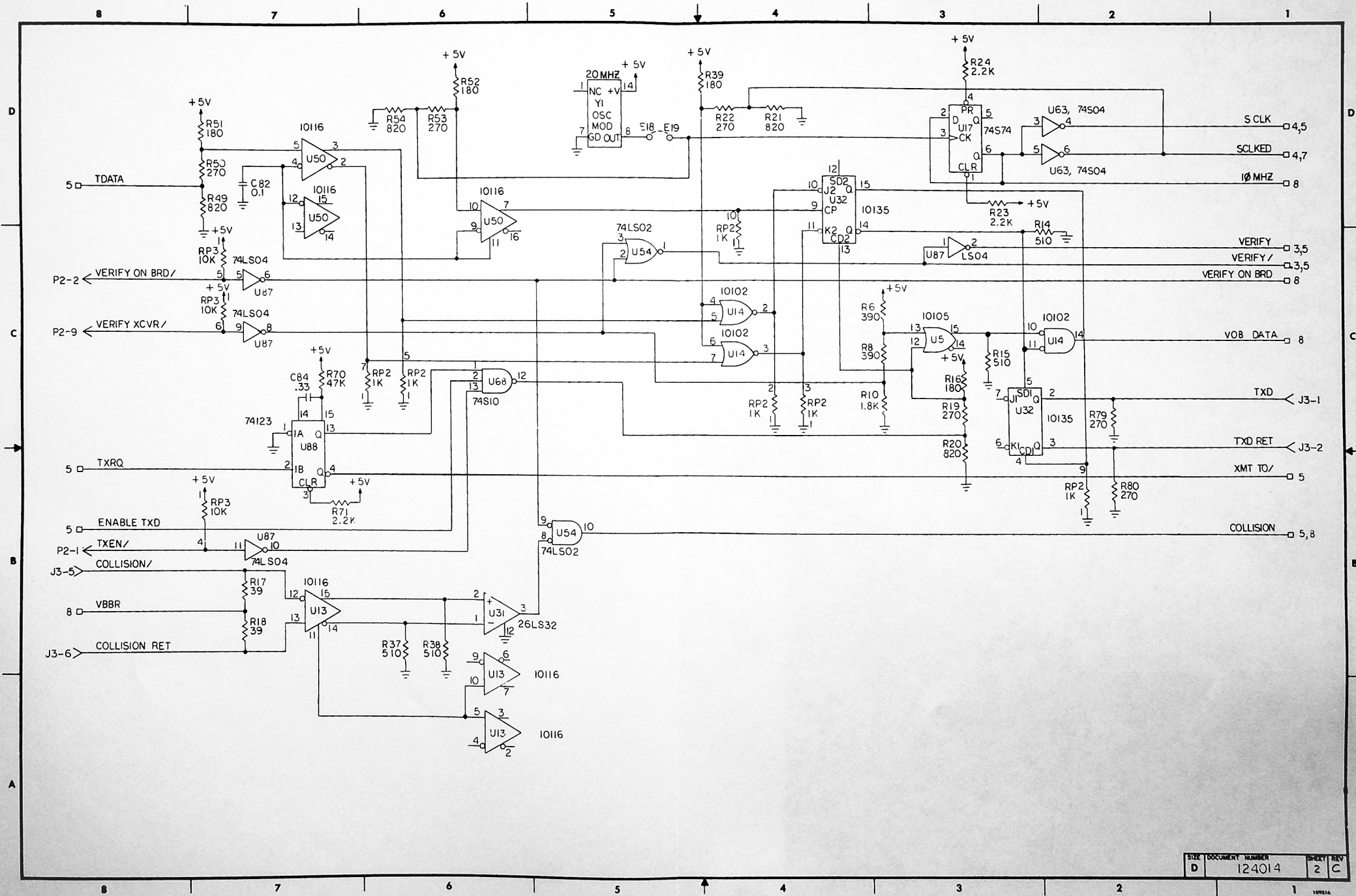
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SIZE: D 13 01 E 124014

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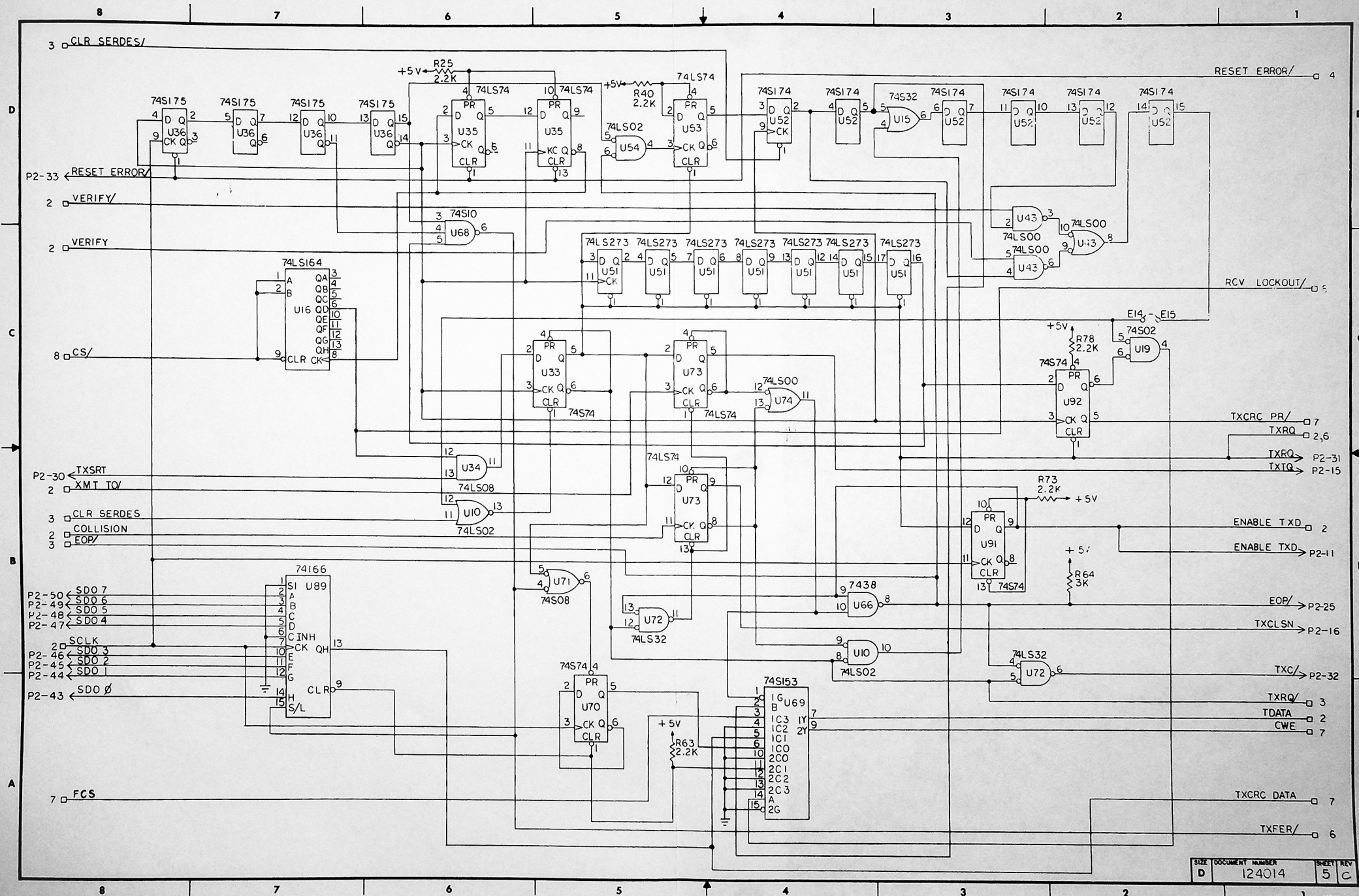
SHEET 1 OF 8

Figure 6-2. SerDes Board Schematic Diagram (Sheet 1 of 8)



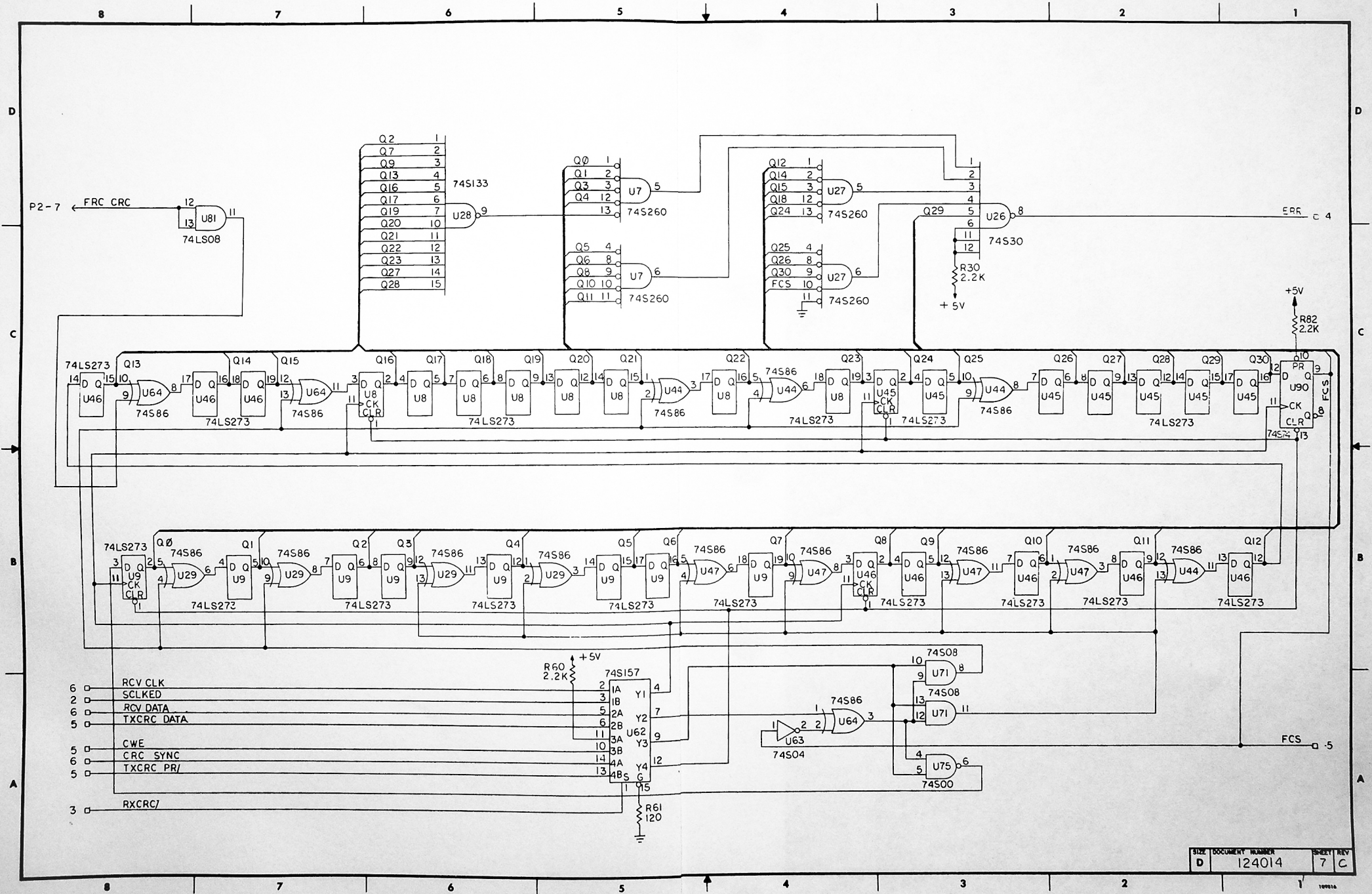
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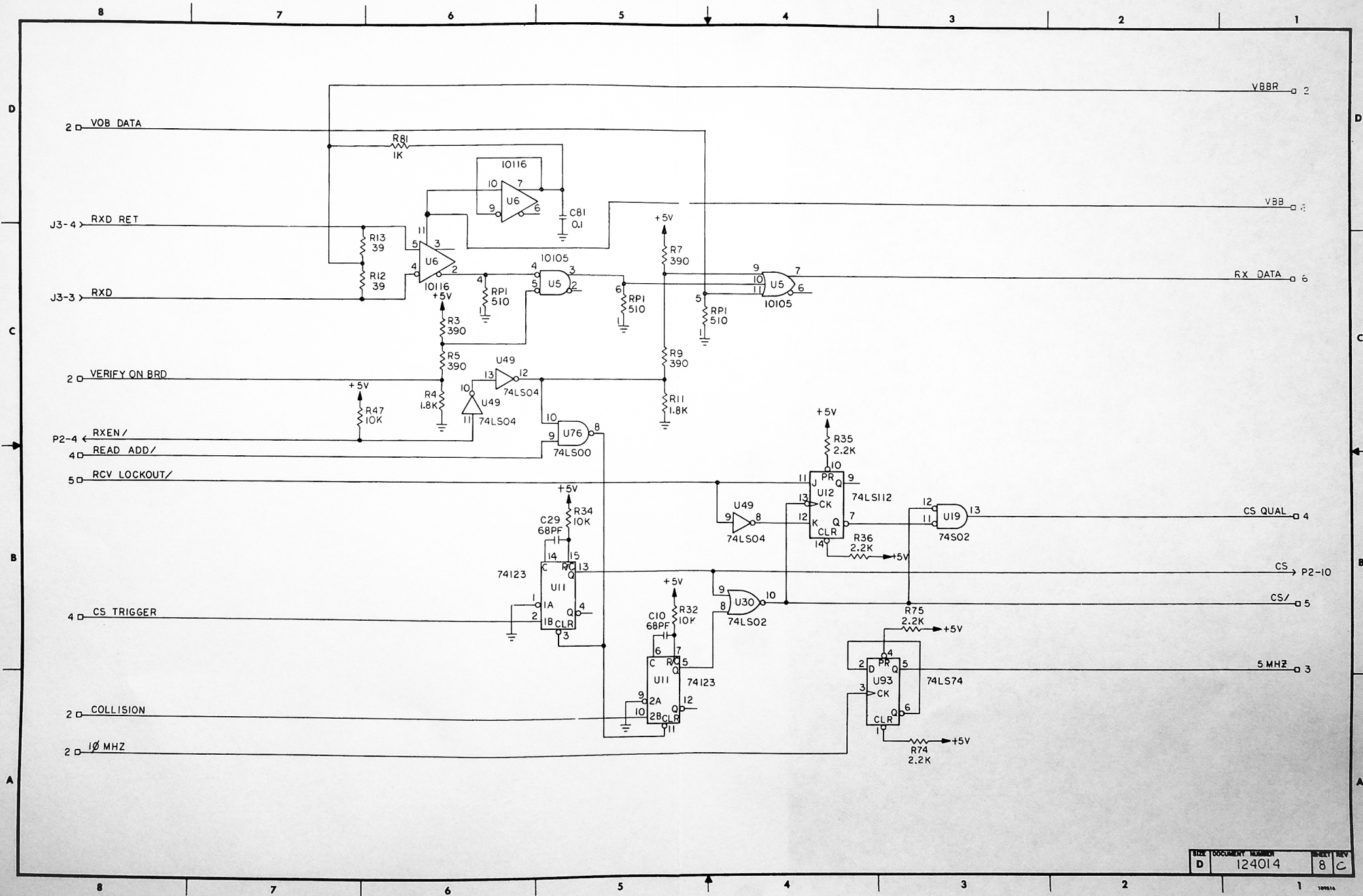
Figure 6-2. SerDes Board Schematic Diagram (Sheet 2 of 8)



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| SIZE | DOCUMENT NUMBER | SHEET | REV |
| D | 124014 | 5 | C |

Figure 6-2. SerDes Board Schematic Diagram (Sheet 5 of 8)





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| Doc ID | 124014 | Rev | 8 |
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Figure 6-2. SerDes Board Schematic Diagram (Sheet 8 of 8)